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signal having gentle waveform in response to an input signal having a sharp waveform, the second drive circuit including a third output transistor connected between the first power supply and the output terminal and a fourth output transistor connected between the second power supply and the output terminal, the third and fourth output transistors having lower impedances than the first and second output transistors, the method comprising the steps of:

generating the output signal in accordance with the input signal using the first drive circuit;

generating a delay signal by delaying the output signal;

generating a control signal for controlling the third and fourth output transistors in accordance with the delay signal and the input signal; and

driving the second drive circuit in accordance with the control signal after the output signal is changed by a predetermined amount.

Just B4
A2
5. (Amended) An output buffer circuit comprising:

a first drive circuit for receiving an input signal having a sharp waveform and generating an output signal that has a gentle waveform and is output from an output terminal of the output buffer circuit;

a second drive circuit connected to the output terminal and having a lower output impedance than the first drive circuit;

a delay circuit, connected to the output terminal, for delaying the output signal and generating a delayed output signal; and